

JAN 05 2006

Attorney's Docket No.: 10559-391001
Client's Ref. No.: P10256 - ADI APD1812-1-US**OFFICIAL COMMUNICATION FACSIMILE:****OFFICIAL FAX NO: (571) 273-8300**

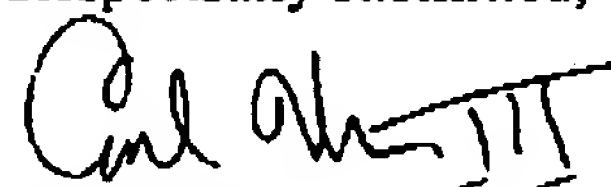
Number of pages including this page 18

Applicant : Ryo Inoue et al.
Serial No. : 09/745,104
Filed : December 20, 2000Art Unit : 2181
Examiner : Tonia L. Meonske
Assignee: Intel Corporation and Analog Devices, Inc.Title : Hardware Loops and A Pipeline System Using Advanced Generation of Loop Parameters
(As Amended)Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet are an Amendment and Transmittal Letter, faxed this 5th day of January, 2006, to the United States Patent and Trademark Office.

Date: January 5, 2006

Respectfully submitted,

Scott C. Harris
Reg. No. 32,030
Attorney for Intel CorporationFish & Richardson P.C.
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San Diego, California 92130
Telephone: (858) 678-5070
Fax: (858) 678-5099**CARL A. KUKKONEN, III**
REG. NO. 42,773

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Attorney's Docket No.: 10559-391001 / P10256 - ADI APD1812-1-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ryo Inoue et al.

Art Unit: 2181

Serial No.: 09/745,104

Examiner: Tonia L. Meonske

Filed: December 20, 2000

Assignee: Intel Corporation and
Analog Devices, Inc.Title: HARDWARE LOOPS AND A PIPELINE SYSTEM USING ADVANCED GENERATION
OF LOOP PARAMETERS (AS AMENDED)**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

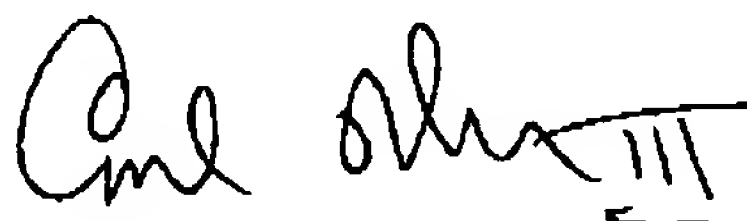
Total

Claims	29	-	30	=	0	\$0
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Independent	6	-	6	=	0	\$0
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TOTAL FEE DUE						\$0
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Respectfully submitted,

Date: 1/5/06

Scott C. Harris

Reg. No. 32,030

Attorney for Intel Corporation

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Attorney's Docket No.: 10559-391001/P10256 - ADI APD1812-1-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ryo Inoue et al. Art Unit: 2181
Serial No.: 09/745,104 Examiner: Tonia L. Meonske
Filed : December 20, 2000 Assignee: Intel Corporation and
Analog Devices, Inc.
Title : HARDWARE LOCPS AND A PIPELINE SYSTEM USING ADVANCED
GENERATION OF LOOP PARAMETERS (AS AMENDED)

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office action mailed November 10, 2005,
please amend the application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 14 of this paper.

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